

PyCUDA: Even Simpler GPU Programming with Python

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Thanks

- Jan Hesthaven (Brown)
- Tim Warburton (Rice)
- Leslie Greengard (NYU)
- PyCUDA contributors
- PyOpenCL contributors
- Nvidia Corporation

Outline

- 1 Scripting GPUs with PyCUDA
- 2 PyOpenCL
- 3 The News
- 4 Run-Time Code Generation
- 5 Showcase

Outline

- 1 Scripting GPUs with PyCUDA
 - PyCUDA: An Overview
 - Do More, Faster with PyCUDA
- 2 PyOpenCL
- 3 The News
- 4 Run-Time Code Generation
- 5 Showcase

Whetting your appetite

```
1 import pycuda.driver as cuda
2 import pycuda.autoinit, pycuda.compiler
3 import numpy
4
5 a = numpy.random.randn(4,4).astype(numpy.float32)
6 a_gpu = cuda.mem_alloc(a.nbytes)
7 cuda.memcpy_htod(a_gpu, a)
```



[This is `examples/demo.py` in the PyCUDA distribution.]

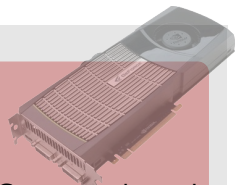
Whetting your appetite

```
1 mod = pycuda.compiler.SourceModule("""
2     __global__ void twice(float *a)
3     {
4         int idx = threadIdx.x + threadIdx.y*4;
5         a[idx] *= 2;
6     }
7     """)
8
9 func = mod.get_function("twice")
10 func(a_gpu, block=(4,4,1))
11
12 a_doubled = numpy.empty_like(a)
13 cuda.memcpy_dtoh(a_doubled, a_gpu)
14 print a_doubled
15 print a
```



Whetting your appetite

```
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2     __global__ void twice(float *a)
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15 print a
```



Compute kernel

Why do Scripting for GPUs?

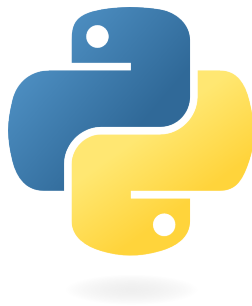
- GPUs are everything that scripting languages are not.
 - Highly parallel
 - Very architecture-sensitive
 - Built for maximum FP/memory throughput
- complement each other
- CPU: largely restricted to control tasks (~1000/sec)
 - Scripting fast enough
- Python + CUDA = **PyCUDA**
- Python + OpenCL = **PyOpenCL**



Scripting: Python

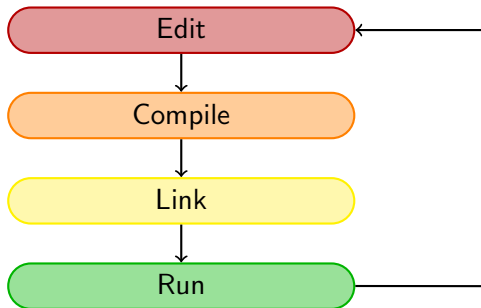
One example of a scripting language: Python

- Mature
- Large and active community
- Emphasizes readability
- Written in widely-portable C
- A 'multi-paradigm' language
- Rich ecosystem of sci-comp related software



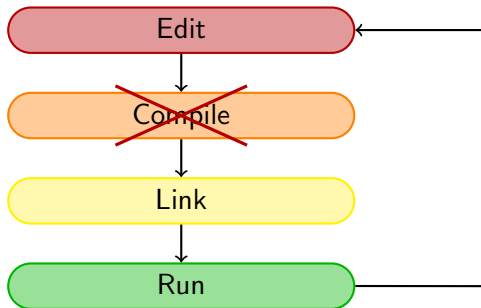
Scripting: Interpreted, not Compiled

Program creation workflow:



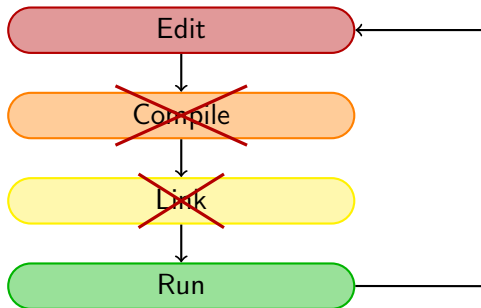
Scripting: Interpreted, not Compiled

Program creation workflow:

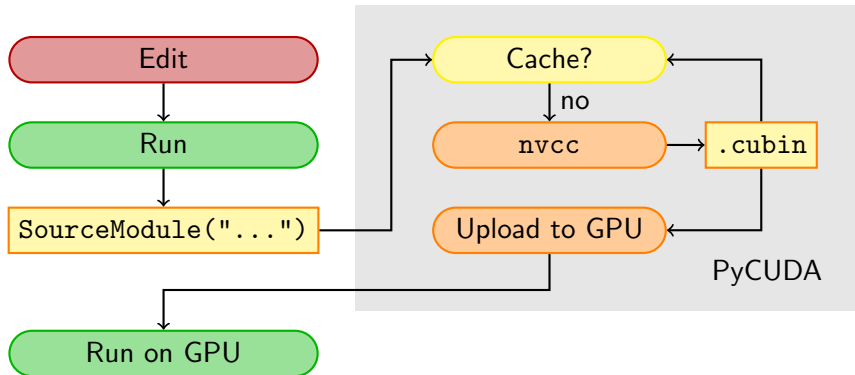


Scripting: Interpreted, not Compiled

Program creation workflow:



PyCUDA: Workflow

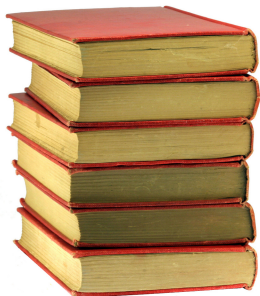


How are High-Performance Codes constructed?

- “Traditional” Construction of High-Performance Codes:
 - C/C++/Fortran
 - Libraries
- “Alternative” Construction of High-Performance Codes:
 - Scripting for ‘brains’
 - GPUs for ‘inner loops’
- Play to the strengths of each programming environment.



PyCUDA Philosophy



- Provide complete access
- Automatically manage resources
- Provide abstractions
- Check for and report errors automatically
- Full documentation
- Integrate tightly with `numpy`

What's this “numpy”, anyway?

Numpy: package for large, multi-dimensional arrays.

- Vectors, Matrices, ...
- $A+B$, $\sin(A)$, $\text{dot}(A,B)$
- `la.solve(A, b)`, `la.eig(A)`
- `cube[:, :, n-k:n+k]`, `cube+5`

All much faster than functional equivalents in Python.

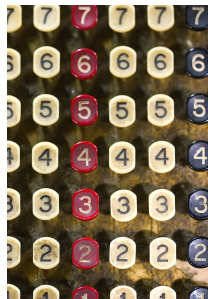
“Python's MATLAB”:
Basis for SciPy, plotting, ...



gpuarray: Simple Linear Algebra

`pycuda.gpuarray`:

- Meant to look and feel just like `numpy`.
 - `gpuarray.to_gpu(numpy_array)`
 - `numpy_array = gpuarray.get()`
- `+`, `-`, `*`, `/`, `fill`, `sin`, `exp`, `rand`,
basic indexing, `norm`, inner product, ...
- Mixed types (`int32 + float32 = float64`)
- `print gpuarray` for debugging.
- Allows access to raw bits
 - Use as kernel arguments, textures, etc.



Whetting your appetite, Part II

```
1 import numpy
2 import pycuda.autoinit
3 import pycuda.gpuarray as gpuarray
4
5 a_gpu = gpuarray.to_gpu(
6     numpy.random.randn(4,4).astype(numpy.float32))
7 a_doubled = (2*a_gpu).get()
8 print a_doubled
9 print a_gpu
```



gpuarray: Elementwise expressions

Avoiding extra store-fetch cycles for elementwise math:

```
from pycuda.curandom import rand as curand
a_gpu = curand((50,))
b_gpu = curand((50,))

from pycuda.elementwise import ElementwiseKernel
lin_comb = ElementwiseKernel(
    "float a, float *x, float b, float *y, float *z",
    "z[i] = a*x[i] + b*y[i]")

c_gpu = gpuarray.empty_like(a_gpu)
lin_comb(5, a_gpu, 6, b_gpu, c_gpu)

assert la.norm((c_gpu - (5*a_gpu+6*b_gpu)).get()) < 1e-5
```

gpucuda: Reduction made easy

Example: A scalar product calculation

```
from pycuda.reduction import ReductionKernel
dot = ReductionKernel(dtype_out=numpy.float32, neutral="0",
                      reduce_expr="a+b", map_expr="x[i]*y[i]",
                      arguments="const float *x, const float *y")
```

```
from pycuda.curandom import rand as curand
x = curand((1000*1000), dtype=numpy.float32)
y = curand((1000*1000), dtype=numpy.float32)
```

```
x_dot_y = dot(x, y).get()
x_dot_y_cpu = numpy.dot(x.get(), y.get())
```

PyCUDA: Vital Information

- <http://mathematician.de/software/pycuda>
- Complete documentation
- MIT License
(no warranty, free for all use)
- Requires: numpy, Python 2.4+
(Win/OS X/Linux)
- Support via mailing list



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OpenCL's perception problem

OpenCL does not presently get the credit it deserves.

- Single abstraction works well for GPUs, CPUs
- Vendor-independence
- Compute Dependency DAG
- A JIT C compiler baked into a library



Introducing... PyOpenCL

- PyOpenCL is “PyCUDA for OpenCL”
- Complete, mature API wrapper
- Has: Arrays, elementwise operations, RNG, ...
- Near feature parity with PyCUDA
- Tested on all available Implementations, OSs
- <http://mathematician.de/software/pyopenc1>



OpenCL

Introducing... PyOpenCL

Same flavor, different recipe:

```
import pyopencl as cl, numpy
```

```
a = numpy.random.rand(50000).astype(numpy.float32)
```

```
ctx = cl.create_some_context()
queue = cl.CommandQueue(ctx)
```

```
a_buf = cl.Buffer(ctx, cl.mem_flags.READ_WRITE, size=a.nbytes)
cl.enqueue_write_buffer(queue, a_buf, a)
```

```
prg = cl.Program(ctx, """
    _kernel void twice(_global float *a)
    {
        int gid = get_global_id(0);
        a[gid] *= 2;
    }""").build()
```

```
prg.twice(queue, a.shape, None, a_buf).wait()
```



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 - Exciting Developments in GPU-Python
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Step 1: Download

Hot off the presses:

- PyCUDA 0.94.1
- PyOpenCL 0.92

All the goodies from this talk, plus

- Supports all new features in CUDA 3.0, 3.1, 3.2rc, OpenCL 1.1
- Allows `printf()` (see example in Wiki)

New stuff shows up in git very quickly.
Still needed: better release schedule.

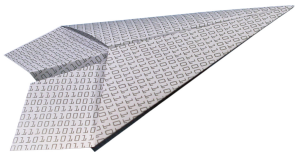


Step 2: Installation

- PyCUDA and PyOpenCL no longer depend on Boost C++
- Eliminates major install obstacle
- Easier to depend on PyCUDA and PyOpenCL
- `easy_install pyopenc1` works on Macs out of the box
- Boost is still there—just not user-visible by default.



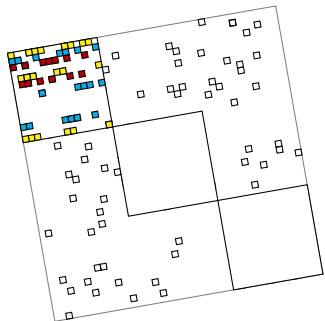
Step 3: Usage



- Complex numbers
 - ... in GPUArray
 - ... in user code
(`pycuda-complex.hpp`)
- If/then/else for GPUArrays
- Support for custom device pointers
- Smarter device picking/context creation
- PyFFT: FFT for PyOpenCL and PyCUDA
- `scikits.cuda`: CUFFT, CUBLAS, CULA

Sparse Matrix-Vector on the GPU

- New feature in 0.94:
Sparse matrix-vector multiplication
- Uses “packeted format”
by Garland and Bell (also
includes parts of their code)
- Integrates with `scipy.sparse`.
- Conjugate-gradients solver
included
 - Deferred convergence
checking



Step 4: Debugging

New in 0.94.1: Support for CUDA gdb:

```
$ cuda-gdb --args python -m  
pycuda.debug demo.py
```

Automatically:

- Sets Compiler flags
- Retains source code
- Disables compiler cache



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 - Writing Code when the most Knowledge is Available
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GPU Programming: Implementation Choices

- Many difficult questions
- Insufficient heuristics
- Answers are hardware-specific and have no lasting value



GPU Programming: Implementation Choices

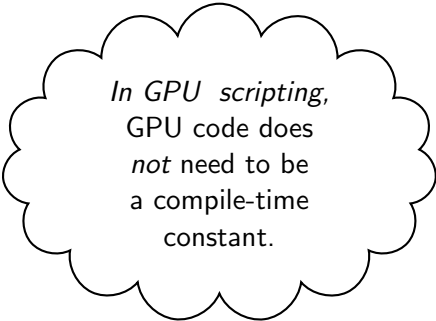
- Many difficult questions
- Insufficient heuristics
- Answers are hardware-specific and have no lasting value



Proposed Solution: Tune automatically for hardware at run time, cache tuning results.

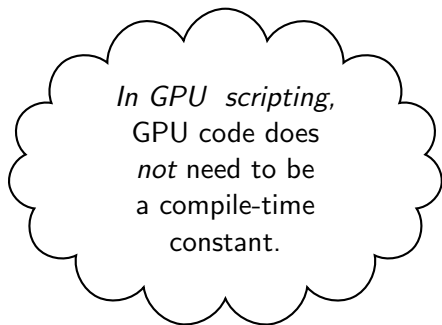
- Decrease reliance on knowledge of hardware internals
- Shift emphasis from tuning *results* to tuning *ideas*

Metaprogramming



*In GPU scripting,
GPU code does
not need to be
a compile-time
constant.*

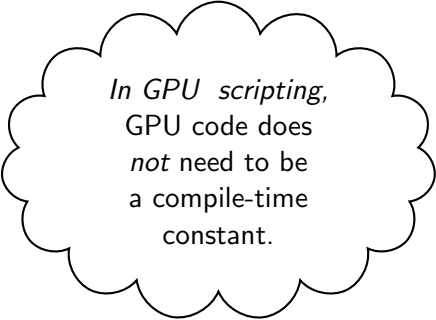
Metaprogramming



(Key: Code is data—it *wants* to be reasoned about at run time)

Metaprogramming

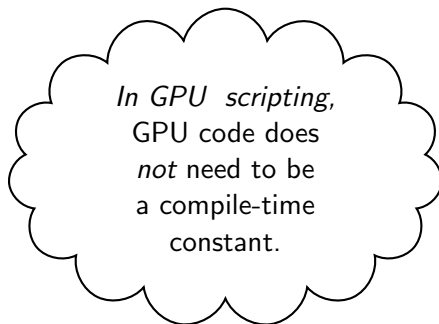
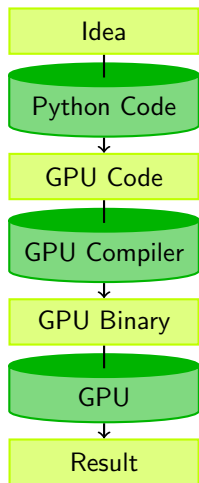
Idea



*In GPU scripting,
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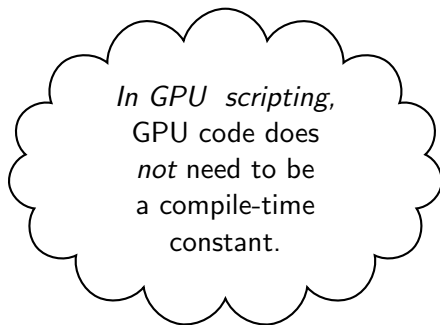
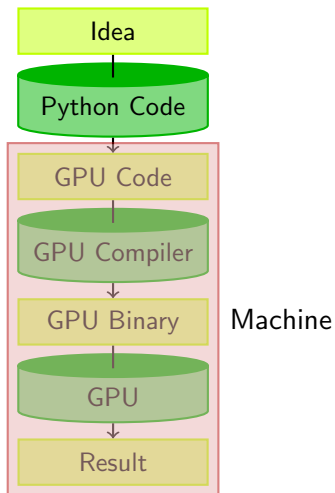
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Metaprogramming



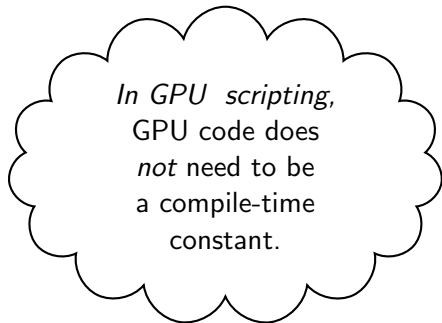
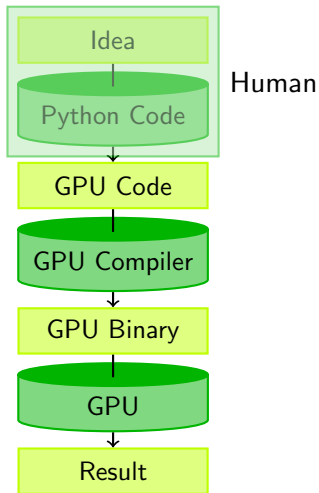
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Metaprogramming



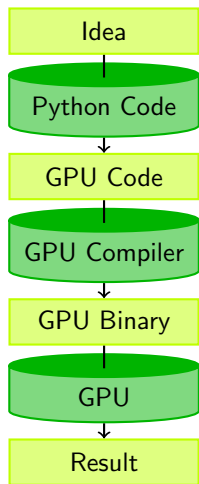
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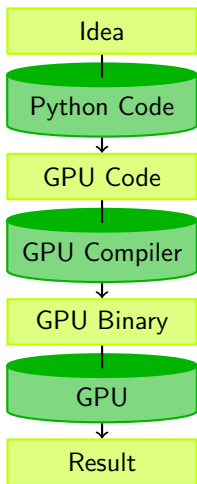


Good for code generation

In GPU scripting, GPU code does not need to be a compile-time constant.

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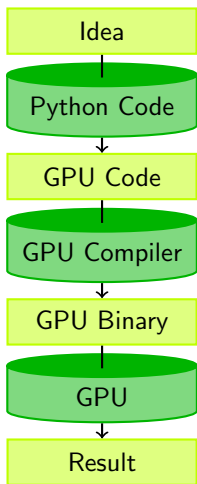
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In **PyCUDA**

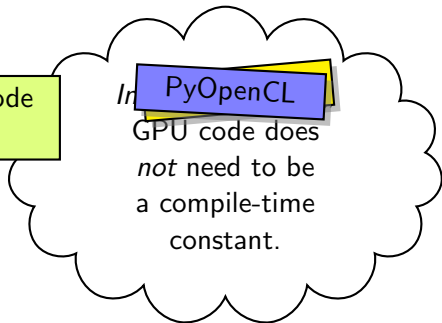
GPU code does *not* need to be a compile-time constant.

(Key: Code is data—it *wants* to be reasoned about at run time)

Metaprogramming



Good for code generation



(Key: Code is data—it *wants* to be reasoned about at run time)

Machine-generated Code

Why machine-generate code?

- Automated Tuning
(cf. ATLAS, FFTW)
- Data types
- Specialize code for given problem
- Constants faster than variables
(→ register pressure)
- Loop Unrolling



RTCG via Templates

```
from jinja2 import Template
tpl = Template("""
    __global__ void twice({{ type_name }} *tgt)
    {
        int idx = threadIdx.x +
            {{ thread_block_size }} * {{ block_size }}
            * blockIdx.x;

        {% for i in range( block_size ) %}
            {% set offset = i* thread_block_size %}
            tgt[idx + {{ offset }}] *= 2;
        {% endfor %}
    }""")

rendered_tpl = tpl.render(
    type_name="float", block_size=block_size,
    thread_block_size=thread_block_size)

smod = SourceModule(rendered_tpl)
```

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 - Python+GPUs in Action
 - Conclusions

Discontinuous Galerkin Method

Let $\Omega := \bigcup_i D_k \subset \mathbb{R}^d$.



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Let $\Omega := \bigcup_i D_k \subset \mathbb{R}^d$.



Goal

Solve a *conservation law* on Ω :

$$u_t + \nabla \cdot F(u) = 0$$

Discontinuous Galerkin Method

Let $\Omega := \bigcup_i D_k \subset \mathbb{R}^d$.



Goal

Solve a *conservation law* on Ω :

$$u_t + \nabla \cdot F(u) = 0$$

Example

Maxwell's Equations: EM field: $E(x, t)$, $H(x, t)$ on Ω governed by

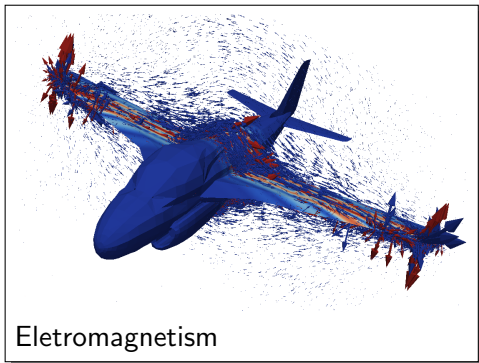
$$\partial_t E - \frac{1}{\varepsilon} \nabla \times H = -\frac{j}{\varepsilon},$$

$$\partial_t H + \frac{1}{\mu} \nabla \times E = 0,$$

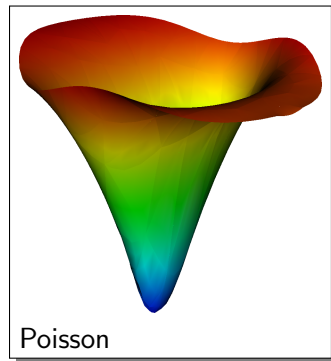
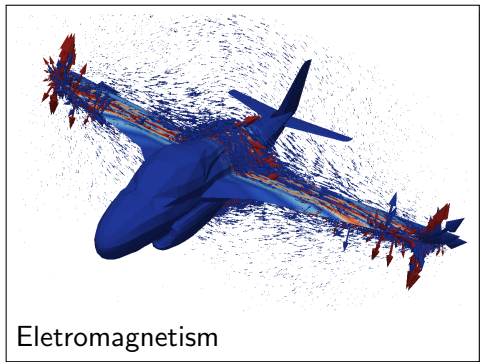
$$\nabla \cdot E = \frac{\rho}{\varepsilon},$$

$$\nabla \cdot H = 0.$$

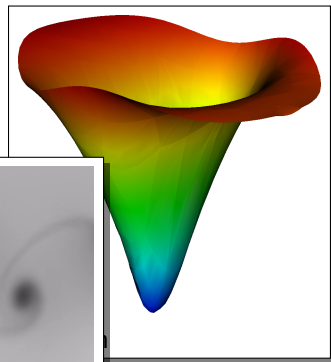
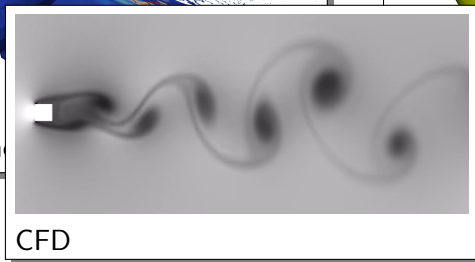
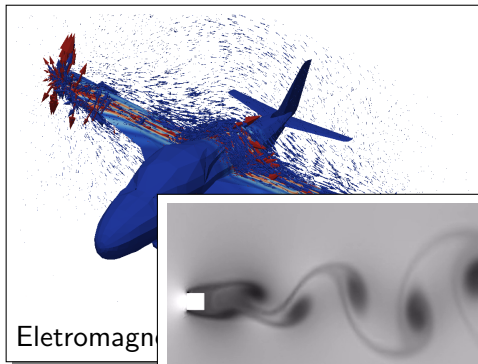
GPU DG Showcase



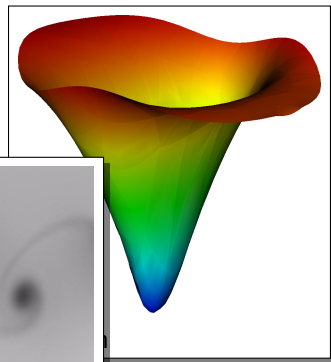
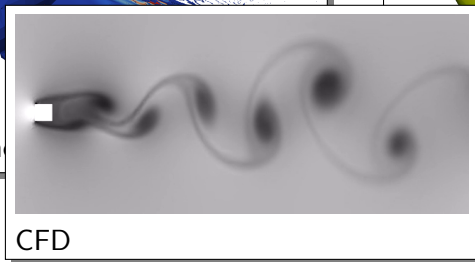
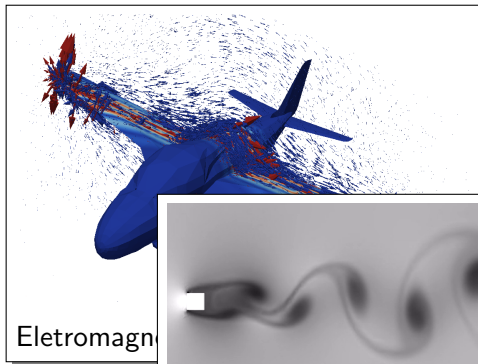
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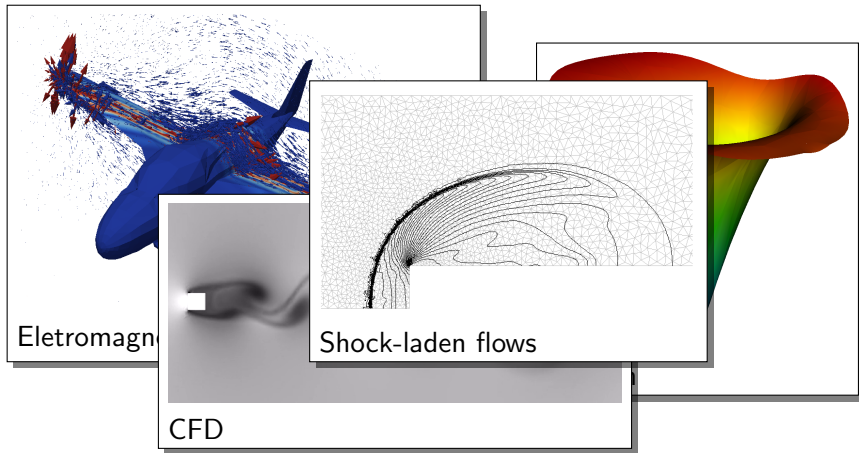
GPU DG Showcase



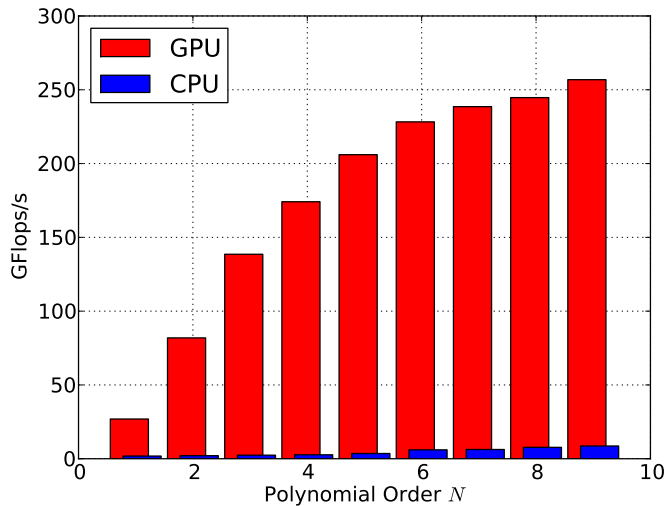
GPU DG Showcase



GPU DG Showcase

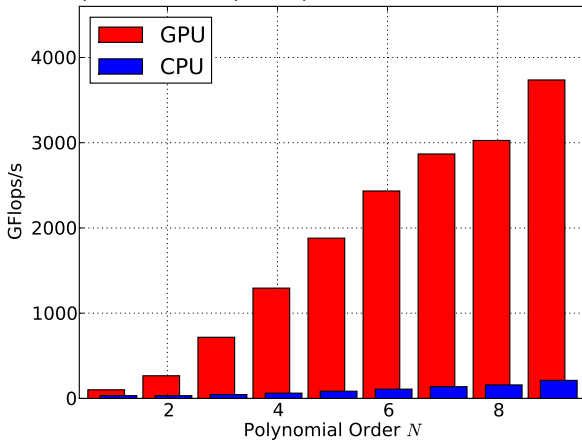


GPU-DG: Performance on GTX280



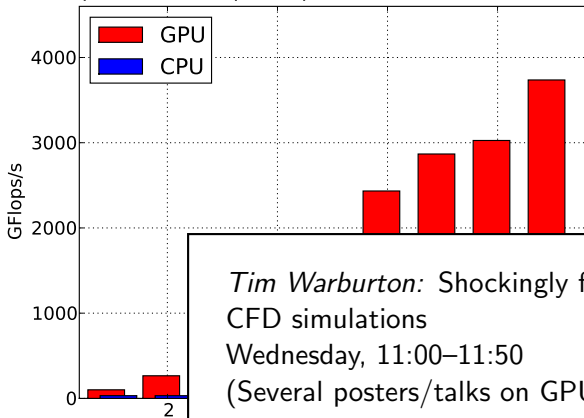
16 T10s vs. $64 = 8 \times 2 \times 4$ Xeon E5472

Flop Rates and Speedups: 16 GPUs vs 64 CPU cores



16 T10s vs. $64 = 8 \times 2 \times 4$ Xeon E5472

Flop Rates and Speedups: 16 GPUs vs 64 CPU cores



Tim Warburton: Shockingly fast and accurate
CFD simulations
Wednesday, 11:00–11:50
(Several posters/talks on GPU-DG at GTC.)

Computational Visual Neuroscience

A High-Throughput Approach to Discovering Good Forms of Visual Representation

David Cox

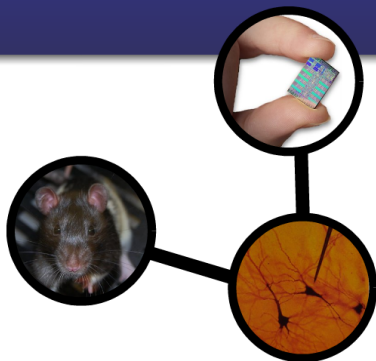
The Rowland Institute at
Harvard

Nicolas Pinto

Jim DiCarlo
MIT BCS



The Rowland Institute at Harvard
HARVARD UNIVERSITY



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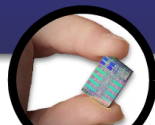
The Rowland Institute at
Harvard

Nicolas Pinto

Jim DiCarlo
MIT BCS



The Rowland
HARVARD UNIVERSITY



Nicolas Pinto: Easy GPU Metaprogramming:
A Case Study in Biologically-Inspired Computer Vision

Thursday, 10:00–10:50, Room A1

Copperhead

```
from copperhead import *
import numpy as np

@cu
def axpy(a, x, y):
    return [a * xi + yi for xi, yi in zip(x, y)]

x = np.arange(100, dtype=np.float64)
y = np.arange(100, dtype=np.float64)

with places.gpu0:
    gpu = axpy(2.0, x, y)

with places.here:
    cpu = axpy(2.0, x, y)
```

Copperhead

```
from copperhead import *
import numpy as np

@cu
def axpy(a, x, y):
    return [a * xi + yi for xi, yi in zip(x, y)]
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```

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```

```
with places.here:
    cpu = axpy(2.0, x, y)
```

Bryan Catanzaro: Copperhead: Data-Parallel Python for the GPU
Wednesday, 15:00–15:50 (next slot!), Room N

Conclusions

- Fun time to be in computational science
- Even more fun with Python and Py{CUDA,OpenCL}
 - With no compromise in performance
- GPUs and scripting work well together
 - Enable Metaprogramming
- The “Right” way to develop computational codes
 - Bake all runtime-available knowledge into code

Where to from here?

More at...

→ <http://mathematician.de/>

CUDA-DG

AK, T. Warburton, J. Bridge, J.S. Hesthaven, “*Nodal Discontinuous Galerkin Methods on Graphics Processors*”, J. Comp. Phys., 2009.

GPU RTCG

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Questions?

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Thank you for your attention!

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